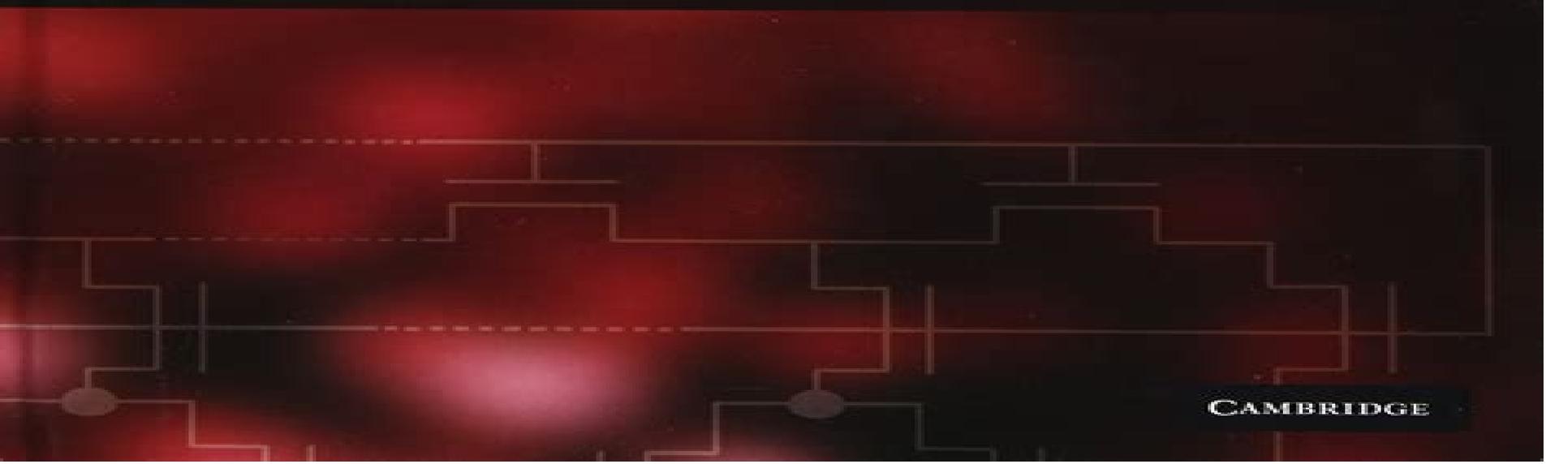
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# CMOS Analog Design Using All-Region MOSFET Modeling

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