CBCS SCHEME

USN | | | | | | | | | | |

15EC42

Fourth Semester B.E. Degree Examination, July/August 2021
Microprocessor

Time: 3 hrs. Max. Marks: 80

Note: *Answer any FIVE full questions.*

Important Note : 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages.
2. Any revealing of identification, appeal to evaluator and /or equations written eg, 42+8 = 50, will be treated as malpractice.

1 a. With a relevant diagram, explain the register organization of 8086. (08 Marks)

b. List any three the advantages of memory segmentation. (03 Marks)

c. The contents of different registers are shown below. Form the effective addresses for the instructions given below. Offset (displacement) = 5000H.
[AX] = 1000H, [BX] = 2000H, [SI] = 3000H, [DI] = 4000H, [BP] = 5000H, [SP] = 6000H, [CS] = 0000H, [DS] = 1000H, [SS] = 2000H, [IP] = 7000H.
i) MOV AX, [5000H]
ii) MOV AX, [BX]
iii) MOV AX, 5000H[Bx]
iv) MOV AX, [BX] [SI]
v) MOV AX, 5000H [BX] [SI]. (05 Marks)

2 a. Explain PUSH AX and POP AX instructions with steps involved. (05 Marks)

b. Write an 8086 ALP to multiply two 8-bit signed numbers. Give example for different cases. (06 Marks)

c. If the machine code for OPCODE MOV is 100010, find the hex codes for,
i) MOV AX, BX
ii) MOV AX, [BX] (05 Marks)

3 a. Explain different string handling instructions. (05 Marks)

b. Write an 8086 ALP for the addition of two 3 × 3 matrices shown below. The matrices are stored in the form of lists (row wise). Store the result of addition in the third list.

$$MAT1 = \begin{bmatrix} 01H & 02H & 03H \\ 04H & 05H & 06H \\ 07H & 08H & 09H \end{bmatrix} \quad MAT2 = \begin{bmatrix} 11H & 22H & 33H \\ 44H & 55H & 66H \\ 77H & 88H & 99H \end{bmatrix}$$ (05 Marks)

c. Explain the following instructions with example
i) AAA ii) SAR iii) NEG. (06 Marks)

4 a. Explain the following assembler directives with example.
i) ORG ii) PTR iii) PROC. (06 Marks)

b. Write an 8086 ALP to convert an 8 bit binary number into equivalent ECD code. (05 Marks)

c. If [CL] = 36, find the contents of register BL after execution of following set of instructions

```
        MOV BL, 1
        MOV AL, 0
UP :    CMP CL, 0
        JZ END
        SUB CL, BL
        INC AL
        ADD BL, 02
        JMP UP
END : MOV BL, AL
```

(05 Marks)

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