

Code No: 118BJ

R13

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B. Tech IV Year II Semester Examinations, September - 2020

EMBEDDED SYSTEMS

(Computer Science and Engineering)

Time: 2 hours

Max. Marks: 75

Answer any Five Questions

All Questions Carry Equal Marks

- 1.a) Briefly describe the distinction between requirements and specification.
b) Explain the formalisms for system design. [7+8]
- 2.a) Explain the memory organization of 8051 in detail.
b) Explain the operation of counters and timers in 8051. [5+10]
- 3.a) Explain the assembly language programming process.
b) Explain the Data Transfer Instructions in 8051 with examples. [5+10]
- 4.a) What are the different logical instructions available in 8051? Write their syntax.
b) Write an assembly language program to 8-bit addition and subtraction. [7+8]
- 5.a) Explain about serial data communication modes in 8051.
b) Draw and explain the A/D and D/A converters. [7+8]
- 6.a) Explain task scheduling mechanisms with an example.
b) Explain how interrupt routines handled in RTOS Environment. [8+7]
- 7.a) Discuss Software development tools of embedded system.
b) Write short notes on the following
i) Testing on host machine
ii) Laboratory tools. [7+8]
- 8.a) Discuss briefly about SHARC architecture.
b) Write about Internet enabled systems. [9+6]

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Joacim Rocklöv



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Table of Contents Embedded Systems Question Papers

1. Understanding the eBook Embedded Systems Question Papers
 - The Rise of Digital Reading Embedded Systems Question Papers
 - Advantages of eBooks Over Traditional Books
2. Identifying Embedded Systems Question Papers
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Embedded Systems Question Papers
 - User-Friendly Interface
4. Exploring eBook Recommendations from Embedded Systems Question Papers
 - Personalized Recommendations
 - Embedded Systems Question Papers User Reviews and Ratings

- Embedded Systems Question Papers and Bestseller Lists
- 5. Accessing Embedded Systems Question Papers Free and Paid eBooks
 - Embedded Systems Question Papers Public Domain eBooks
 - Embedded Systems Question Papers eBook Subscription Services
 - Embedded Systems Question Papers Budget-Friendly Options
- 6. Navigating Embedded Systems Question Papers eBook Formats
 - ePub, PDF, MOBI, and More
 - Embedded Systems Question Papers Compatibility with Devices
 - Embedded Systems Question Papers Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Embedded Systems Question Papers
 - Highlighting and Note-Taking Embedded Systems Question Papers
 - Interactive Elements Embedded Systems Question Papers
- 8. Staying Engaged with Embedded Systems Question Papers
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Embedded Systems Question Papers
- 9. Balancing eBooks and Physical Books Embedded Systems Question Papers
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Embedded Systems Question Papers
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Embedded Systems Question Papers
 - Setting Reading Goals Embedded Systems Question Papers
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Embedded Systems Question Papers
 - Fact-Checking eBook Content of Embedded Systems Question Papers
 - Distinguishing Credible Sources

13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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