

DISCRETE-TIME SIGNAL PROCESSING

INTERNATIONAL
Edition

Second Edition



Alan V. Oppenheim • Ronald W. Schafer
with John R. Buck



PRENTICE HALL SIGNAL PROCESSING SERIES

ALAN V. OPPENHEIM, SERIES EDITOR

Digital Systems Alan

M Tight



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Correct Hardware Design and Verification Methods George J. Milne, Laurence Pierre, 1993-05-12 These proceedings contain the papers presented at the Advanced Research Working Conference on Correct Hardware Design Methodologies held in Arles France in May 1993 and organized by the ESPRIT Working Group 6018 CHARME 2 and the Universit de Provence Marseille in cooperation with IFIP Working Group 10.2 Formal verification is emerging as a plausible alternative to exhaustive simulation for establishing correct digital hardware designs The validation of functional and timing behavior is a major bottleneck in current VLSI design systems slowing the arrival of products in the marketplace with its associated increase in cost From being a predominantly academic area of study until a few years ago formal design and verification techniques are now beginning to migrate into industrial use As we are now witnessing an increase in activity in this area in both academia and industry the aim of this working conference was to bring together researchers and users from both communities

Digital Systems and Applications Vojin G. Oklobdzija, 2017-12-19 New design architectures in computer systems have surpassed industry expectations Limits which were once thought of as fundamental have now been broken Digital Systems and Applications details these innovations in systems design as well as cutting edge applications that are emerging to take advantage of the fields increasingly sophisticated capabilities This book features new chapters on parallelizing iterative heuristics stream and wireless processors and lightweight embedded systems This fundamental text Provides a clear focus on computer systems architecture and applications Takes a top level view of system organization before moving on to architectural and organizational concepts such as superscalar and vector processor VLIW architecture as well as new trends in multithreading and multiprocessing includes an entire section dedicated to embedded systems and their applications Discusses topics such as digital signal processing applications circuit implementation aspects parallel I/O algorithms and operating systems Concludes with a look at new and future directions in computing Features articles that describe diverse aspects of computer usage and potentials for use Details implementation and performance enhancing techniques such as branch prediction register renaming and virtual memory Includes a section on new directions in computing and their penetration into many new fields and aspects of our daily lives

Digital Signal Processing Algorithms Hari Krishna, 2017-11-22 Digital Signal Processing Algorithms describes computational number theory and its applications to deriving fast algorithms for digital signal processing It demonstrates the importance of computational number theory in the design of digital signal processing algorithms and clearly describes the nature and structure of the algorithms themselves The book has two primary focuses first it establishes the properties of discrete time sequence indices and their corresponding fast algorithms and second it investigates the properties of the discrete time sequences and the corresponding fast algorithms for processing these sequences Digital Signal Processing Algorithms examines three of the most common computational tasks that occur in digital signal processing namely cyclic convolution acyclic convolution and discrete Fourier

transformation The application of number theory to deriving fast and efficient algorithms for these three and related computationally intensive tasks is clearly discussed and illustrated with examples Its comprehensive coverage of digital signal processing computer arithmetic and coding theory makes Digital Signal Processing Algorithms an excellent reference for practicing engineers The authors intent to demystify the abstract nature of number theory and the related algebra is evident throughout the text providing clear and precise coverage of the quickly evolving field of digital signal processing

A Simulator for the Evaluation of Digital System Reliability Stanford University. Stanford Electronics Laboratories. Digital Systems Laboratory, Peter Alan Thompson, 1977 This report describes a simulation package designed to evaluate the reliability of digital systems The simulator can be used to model many different types of systems at varying levels of detail The user is given much freedom to use the elements of the model in the way best suited to simulating the operation of a system in the presence of faults The simulation package then generates random faults in the model and uses a Monte Carlo analysis to obtain curves of reliability Three examples are given of simulations of digital systems which have redundancy The difference between this type of simulation and other simulation techniques is discussed

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