8086 Microprocessor Registers

AX	AH	AL	Accumulator
BX	BH	BL	Base Register
CX	CH	CL	Count Register
DX	DH	DL	Data Register

SP	Stack Pointer
BP	Base Pointer
SI	Source Index
DI	Destination Index

FLAGH	FLAGL	Status Flags
IP		Instruction Pointer

CS	Code Segment Register
DS	Data Segment Register
SS	Stack Segment Register
ES	Extra Segment Register

JunctionByte

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Muhammad Ali Mazidi,Janice Gillispie Mazidi

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microprocessors the architecture the programming and the system design The 8086 microprocessor is described in detail along with glimpses of 8088 80186 and 80188 microprocessors The various peripheral controllers for 8086 88 are also discussed Other topics that are related to the syllabus but not explicitly mentioned are included in the appendices Key Features Programs are given and the related theory is discussed within the same section thereby maintaining a smooth flow and also eliminating the need for a separate section on the practical experiments for the subject of Microprocessors and Interfacing Laboratory Both DOS based programs as well as kit programs are given Algorithms and flowcharts are given before DOS based programs for easy understanding of the program logic *The X86 Microprocessor, 2e* Lyla B. Das,2014 This second edition of The x86 Microprocessors has been revised to present the hardware and software aspects of the subject in a logical and concise manner Designed for an undergraduate course on the 16 bit microprocessor and Pentium processor the book provides a detailed analysis of the x86 family architecture while laying equal emphasis on its programming and interfacing attributes The book also covers 8051 Microcontroller and its applications completely *Proceedings of Frontiers in Education 1996* IEEE, Education Society Staff,Ieee Education Society,1996-11 *Computer Organization and Architecture* William Stallings,2010 KEY BENEFIT Learn the fundamentals of processor and computer design from the newest edition of this award winning text KEY TOPICS Introduction Computer Evolution and Performance A Top Level View of Computer Function and Interconnection Cache Memory Internal Memory Technology External Memory I O Operating System Support Computer Arithmetic Instruction Sets Characteristics and Functions Instruction Sets Addressing Modes and Formats CPU Structure and Function RISCs Instruction Level Parallelism and Superscalar Processors Control Unit Operation Microprogrammed Control Parallel Processing Multicore Architecture Online Chapters Number Systems Digital Logic Assembly Language Assemblers and Compilers The IA 64 Architecture MARKET Ideal for professionals in computer science computer engineering and electrical engineering *Standard Handbook of Electronic Engineering, 5th Edition* Donald Christiansen,Charles K. Alexander,Ronald K. Jurgen,2005-01-10 The Standard Handbook of Electronics Engineering has defined its field for over thirty years Spun off in the 1960 s from Fink s Standard Handbook of Electrical Engineering the Christiansen book has seen its markets grow rapidly as electronic engineering and microelectronics became the growth engine of digital computing The EE market has now undergone another seismic shift away from computing and into communications and media The Handbook will retain much of its evergreen basic material but the key applications sections will now focus upon communications networked media and medicine the eventual destination of the majority of graduating EEs these days Book Review Index ,2003 Vols 8 10 of the 1965 1984 master cumulation constitute a title index **The Cumulative Book Index** ,1996 A world list of books in the English language **The British National Bibliography** Arthur James Wells,2004 **The 80x86 IBM PC and Compatible Computers Volumes I & II** Muhammad Ali Mazidi,Janice Gillispie Mazidi,2000

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