

# GBGS SCHEME

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15EC42



## Fourth Semester B.E. Degree Examination, July/August 2021 Microprocessor

Time: 3 Hrs.

Max. Marks: 80

Note: Answer any FIVE full questions.

1. a. With a relevant diagram, explain the register organization of 8086. (08 Marks)  
b. List any three the advantages of memory segmentation. (03 Marks)  
c. The contents of different registers are shown below. Form the effective addresses for the instructions given below. Offset (displacement) = 5000H.  
[AX] = 1000H, [BX] = 2000H, [SI] = 3000H, [DI] = 4000H, [BP] = 5000H, [SP] = 6000H, [CS] = 0000H, [DS] = 1000H, [SS] = 2000H, [IP] = 7000H.  
i) MOV AX, [5000H]  
ii) MOV AX, [BX]  
iii) MOV AX, 5000H[BX]  
iv) MOV AX, [BX][SI]  
v) MOV AX, 5000H [BX] [SI]. (05 Marks)
2. a. Explain PUSH AX and POP AX instructions with steps involved. (05 Marks)  
b. Write an 8086 ALP to multiply two 8 bit signed numbers. Give example for different cases. (06 Marks)  
c. If the machine code for OP CODE MOV is 100010, find the hex codes for,  
i) MOV AX, BX  
ii) MOV AX, [BX] (05 Marks)
3. a. Explain different string handling instructions. (05 Marks)  
b. Write an 8086 ALP for the addition of two  $3 \times 3$  matrices shown below. The matrices are stored in the form of lists (row wise). Store the result of addition in the third list.  
$$\text{MAT1} = \begin{bmatrix} 01\text{H} & 02\text{H} & 03\text{H} \\ 04\text{H} & 05\text{H} & 06\text{H} \\ 07\text{H} & 08\text{H} & 09\text{H} \end{bmatrix} \quad \text{MAT2} = \begin{bmatrix} 11\text{H} & 22\text{H} & 33\text{H} \\ 44\text{H} & 55\text{H} & 66\text{H} \\ 77\text{H} & 88\text{H} & 99\text{H} \end{bmatrix}$$
 (05 Marks)  
c. Explain the following instructions with example  
i) AAA ii) SAR iii) NEG. (06 Marks)
4. a. Explain the following assembler directives with example.  
i) ORG ii) PTR iii) PROC. (06 Marks)  
b. Write an 8086 ALP to convert an 8 bit binary number into equivalent ECD code. (05 Marks)  
c. If [CL] = 56, find the contents of register BL, after execution of following set of instructions  
MOV BL, 1  
MOV AL, 0  
UP : CMP CL, 0  
JZ END  
SUB CL, BL  
INC AL  
ADD BL, 02  
JMP UP  
END : MOV BL, AL (05 Marks)

# Microprocessor Question Paper Vtu

**S. K. Venkata Ram**



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