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Digital Design and Computer Architecture, ARM Edition Sarah Harris, David Harris, 2015-04-09 *Digital Design and Computer Architecture ARM Edition* covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor By the end of this book readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing an ARM processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I/O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines

digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Features side by side examples of the two most prominent Hardware Description Languages HDLs SystemVerilog and VHDL which illustrate and compare the ways each can be used in the design of digital systems Includes examples throughout the text that enhance the reader s understanding and retention of key concepts and techniques The Companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises

Embedded SoPC Design with Nios II Processor and Verilog Examples

Pong P. Chu,2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology

Digital Filter Design Solutions Jolyon M. De Freitas,2005

Take advantage of the widest possible range of filtering techniques and still keep design time to a minimum with this book and CD ROM toolkit The practical knowledge presented in the book enables you to take control of your projects using the filter coefficients included on the CD ROM You get 260 digital filters that are ready to use and have been fully characterized in terms of their frequency response step response impulse response and pass band characteristics Performance parameters

such as step response rise time overshoot settling time dc accuracy and those related to noise propagation through the filter have been tabulated to allow you full control of your filtering application [Embedded SoPC Design with Nios II Processor and VHDL Examples](#) Pong P. Chu,2011-08-29 The book is divided into four major parts Part I covers HDL constructs and synthesis of basic digital circuits Part II provides an overview of embedded software development with the emphasis on low level I O access and drivers Part III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration **Books in Print** ,1994 [Subject Guide to Books in Print](#) ,1996 **Digital Design** ,1983

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The Michigan Technic ,1950 **Choice** ,1983 *The Handbook of Computers and Computing* Arthur H. Seidman,Ivan Flores,1984 **Information Technology for Management** Efraim Turban,Ephraim R. McLean,James C. Wetherbe,2002 Taking a practical managerial oriented approach this text stresses how information technology provides solutions to organisational problems and challenges and emphasises the innovative use of information technology **The Publishers' Trade List Annual** ,1985

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