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Digital Logic and Computer Design

M. Morris Mano



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**Vincent P. Heuring, Harry Frederick
Jordan**



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Computer engineering Moshe Morris Mano,1988 *Computer Engineering* M. Morris Mano,1988 An introduction to the hardware concepts needed to analyze and design digital systems and the principles of computer hardware organization and design

Computer System Architecture M. Morris Mano,1993 Dealing with computer architecture as well as computer organization and design this fully updated book provides the basic knowledge necessary to understand the hardware operation of digital computers Written to aid electrical engineers computer engineers and computer scientists the volume includes KEY FEATURES the computer architecture organization and design associated with computer hardware the various digital components used in the organization and design of digital computers detailed steps that a designer must go through in order to design an elementary basic computer the organization and architecture of the central processing unit the organization and architecture of input output and memory the concept of multiprocessing two new chapters on pipeline and vector processing two sections devoted completely to the reduced instruction set computer RISC and sample worked out problems to clarify topics

Logic and Computer Design Fundamentals, Global Edition Morris R. Mano,Charles R. Kime,Tom Martin,2015-09-23 Featuring a strong emphasis on the fundamentals underlying contemporary logic design using hardware description languages synthesis and verification this book focuses on the ever evolving applications of basic computer design concepts with strong connections to real world technology

Handbook of Digital Techniques for High-Speed Design ,2007-09 Logic and Computer Design Fundamentals M. Morris Mano,Charles R. Kime,2004

Encyclopedia of Microcomputers Allen Kent,James G. Williams,1990-01-26 The Encyclopedia of Microcomputers serves as the ideal companion reference to the popular Encyclopedia of Computer Science and Technology Now in its 10th year of publication this timely reference work details the broad spectrum of microcomputer technology including microcomputer history explains and illustrates the use of microcomputers throughout academe business government and society in general and assesses the future impact of this rapidly changing technology

Introduction to Computer Engineering Lawrence W. Potts,Franco P. Preparata,1985

Digital Design M. Morris Mano,1991 For this edition eight chapters have been substantially revised by adding new topics and deleting those that are obsolete An entirely new chapter presents IEEE Standard graphic symbols for logic elements recommended by ANSI IEEE Standard 91 1984 In addition new problems have been formulated for the first seven chapters and new experiments have been added to Chapter 11

Computer Systems Design and Architecture Vincent P. Heuring,Harry Frederick Jordan,1997 This text serves as an introduction to and a survey of the common commercial architectures It was created with a strong electrical and computer engineering perspective including current topics such as pipelined processor design memory hierarchy and in

FPGA Implementations of Neural Networks Amos R. Omondi,Jagath C. Rajapakse,2006-10-04 During the 1980s and early 1990s there was significant work in the design and implementation of hardware neurocomputers Nevertheless most of these efforts

may be judged to have been unsuccessful at no time have have ha ware neurocomputers been in wide use This lack of success may be largely attributed to the fact that earlier work was almost entirely aimed at developing custom neurocomputers based on ASIC technology but for such niche eas this technology was never suf ciently developed or competitive enough to justify large scale adoption On the other hand gate arrays of the period m tioned were never large enough nor fast enough for serious arti cial neur network ANN applications But technology has now improved the capacity and performance of current FPGAs are such that they present a much more realistic alternative Consequently neurocomputers based on FPGAs are now a much more practical proposition than they have been in the past This book summarizes some work towards this goal and consists of 12 papers that were selected after review from a number of submissions The book is nominally divided into three parts Chapters 1 through 4 deal with foundational issues Chapters 5 through 11 deal with a variety of implementations and Chapter 12 looks at the lessons learned from a large scale project and also reconsiders design issues in light of current and future technology

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Electrical Computer Engineering University of Wisconsin--Madison. Department of Electrical and Computer Engineering, 1986

Bibliographic Guide to Computer Science ,1991

Computer Engineering ,1960

Accountancy ,1988

An Introduction to Modern Electronics William L. Faissler, 1991-03-19 Offers a complete grounding in the principles and techniques of modern electronics Designed to provide even beginning students with the knowledge and skills necessary for building useful and interesting circuits either in a laboratory situation or on their own Concentrates on techniques and devices currently used in modern equipment and special attention is paid to the basic ideas and techniques used with important types of circuits A substantial portion of the book is devoted to explaining the vocabulary and information presented in data sheets for these circuits By instructing students in these techniques and familiarizing them with the ins and outs of electronic literature it provides a sound introduction to the field and a means of keeping up with its extremely rapid changes

Design Recipes for FPGAs: Using Verilog and VHDL Peter Wilson, 2011-02-24 Design Recipes for FPGAs Using Verilog and VHDL provides a rich toolbox of design techniques and templates to solve practical every day problems using FPGAs Using a modular structure the book gives easy to find design techniques and templates at all levels together with functional code Written in an informal and easy to grasp style it goes beyond the principles of FPGA s and hardware description languages to actually demonstrate how specific designs can be synthesized simulated and downloaded onto an FPGA This book s easy to find structure begins with a design application to demonstrate the key building blocks of FPGA design and how to connect them enabling the experienced

FPGA designer to quickly select the right design for their application while providing the less experienced a road map to solving their specific design problem The book also provides advanced techniques to create real world designs that fit the device required and which are fast and reliable to implement This text will appeal to FPGA designers of all levels of experience It is also an ideal resource for embedded system development engineers hardware and software engineers and undergraduates and postgraduates studying an embedded system which focuses on FPGA design A rich toolbox of practical FPGA design techniques at an engineer s finger tips Easy to find structure that allows the engineer to quickly locate the information to solve their FPGA design problem and obtain the level of detail and understanding needed

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Electric Circuits James William Nilsson,Susan A. Riedel,2005

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