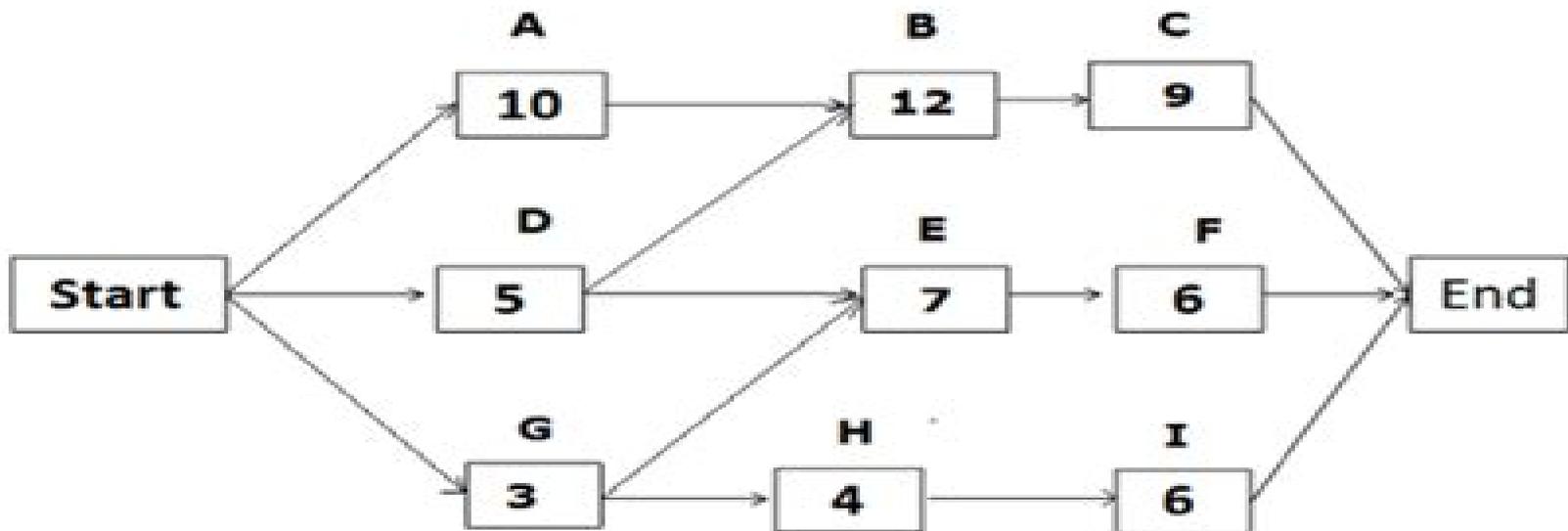


3. Describe critical path analysis. How is the critical path calculated? Based on the below network diagram, identify the total paths, critical path, and float for each path.



# Critical Path Analysis Questions And Answers

**JG Myers**



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web circuit model with parasitic parametric red color is shown in fig 1 c p is the sum of the diode junction capacitance and the paralleled capacitance of load inductor l d is the total parasitic

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web this circuit is used to test n mosfets the power kind e g irf830 whether it works or not if it is not working the led will not flash if mosfet is working it will operate in the astable multivibrator circuit causing the led to flash here is the circuit diagram

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web follow the below steps to conduct the diode test 1 for this test set the multimeter in the diode mode 2 for n channel mosfet connect the red probe positive to the source and black one at the drain common the body diode is in forward bias condition this way

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led as it receives pulses from the mosfet drain