



The Art Of Hardware Architecture Springer

Vojin G. Oklobdzija



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The Art of Hardware Architecture Mohit Arora,2011-10-09 This book highlights the complex issues tasks and skills that must be mastered by an IP designer in order to design an optimized and robust digital circuit to solve a problem The techniques and methodologies described can serve as a bridge between specifications that are known to the designer and RTL code that is final outcome reducing significantly the time it takes to convert initial ideas and concepts into right first time silicon Coverage focuses on real problems rather than theoretical concepts with an emphasis on design techniques across various aspects of chip design [The Art of Hardware Architecture](#) ,2011-10-09 **Effective Coding with VHDL** Ricardo Jasinski,2016-05-27 A guide to applying software design principles and coding practices to VHDL to improve the readability maintainability and quality of VHDL code This book addresses an often neglected aspect of the creation of VHDL designs A VHDL description is also source code and VHDL designers can use the best practices of software development to write high quality code and to organize it in a design This book presents this unique set of skills teaching VHDL designers of all experience levels how to apply the best design principles and coding practices from the software world to the world of hardware The concepts introduced here will help readers write code that is easier to understand and more likely to be correct with improved readability maintainability and overall quality After a brief review of VHDL the book presents fundamental design principles for writing code discussing such topics as design quality architecture modularity abstraction and hierarchy Building on these concepts the book then introduces and provides recommendations for each basic element of VHDL code including statements design units types data objects and subprograms The book covers naming data objects and functions commenting the source code and visually presenting the code on the screen All recommendations are supported by detailed rationales Finally the book explores two uses of VHDL synthesis and testbenches It examines the key characteristics of code intended for synthesis distinguishing it from code meant for simulation and then demonstrates the design and implementation of testbenches with a series of examples that verify different kinds of models including combinational sequential and FSM code Examples from the book are also available on a companion website enabling the reader to experiment with the complete source code **Hardware Security** Swarup Bhunia,Mark M. Tehranipoor,2018-10-30 **Hardware Security** A Hands On Learning Approach provides a broad comprehensive and practical overview of hardware security that encompasses all levels of the electronic hardware infrastructure It covers basic concepts like advanced attack techniques and countermeasures that are illustrated through theory case studies and well designed hands on laboratory exercises for each key concept The book is ideal as a textbook for upper level undergraduate students studying computer engineering computer science electrical engineering and biomedical engineering but is also a handy reference for graduate students researchers and industry professionals For academic courses the book contains a robust suite of teaching ancillaries Users will be able to access schematic layout and design files for a printed circuit board for hardware hacking i e the HaHa

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Embedded Cryptographic Hardware Nadia Nedjah, Luiza de Macedo Mourelle, 2004 Modern cryptology which is the basis of information security techniques started in the late 70 s and developed in the 80 s As communication networks were spreading deep into society the need for secure communication greatly promoted cryptographic research The need for fast but secure cryptographic systems is growing bigger Therefore dedicated systems for cryptography are becoming a key issue for designers With the spread of reconfigurable hardware such as FPGAs hardware implementations of cryptographic algorithms become cost effective The focus of this book is on all aspects of embedded cryptographic hardware Of special interest are contributions that describe new secure and fast hardware implementations and new efficient algorithms methodologies and protocols for secure communications This book is organised in two parts The first part is dedicated to embedded hardware of cryptosystems while the second part focuses on new algorithms for cryptography design methodologies and secure protocols

[High Efficiency Video Coding and Other Emerging Standards](#) K.R. Rao, J.J. Hwang, D.N. Kim, 2022-09-01 High Efficiency Video Coding and Other Emerging Standards provides an overview of high efficiency video coding HEVC and all its extensions and profiles There are nearly 300 projects and problems included and about 400 references related to HEVC alone Next generation video coding NGVC beyond HEVC is also described Other video coding standards such as AVS2 DAALA THOR VP9 Google DIRAC VC1 and AV1 are addressed and image coding standards such as JPEG JPEG LS JPEG2000 JPEG XR JPEG XS JPEG XT and JPEG Pleno are also listed Understanding of these standards and their implementation is facilitated by overview papers standards documents reference software software manuals test sequences source codes tutorials keynote speakers panel discussions reflector and ftp web sites all in the public domain Access to these categories is also provided

Electrical & Electronics Abstracts, 1997

Cumulated Index to the Books, 1999 *Genetic and Evolutionary Computation for Image Processing and Analysis* Stefano Cagnoni, 2008

The Computer Engineering Handbook Vojin G. Oklobdzija, 2019-07-05 After nearly six years as the field s leading reference the second edition of this award winning handbook reemerges with completely updated content and a brand new format The Computer Engineering Handbook Second Edition is now offered as a set of two carefully focused books that together encompass all aspects of the field In addition to complete updates throughout the book to reflect the

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The Cumulative Book Index, 1998 A world list of books in the English language

[Bibliographic Guide to Art and Architecture](#) New York Public Library. Art and Architecture Division, 1975

The Architecture of Computer Hardware and Systems Software Irv Englander, 2003 Fundamental principles that will keep you on the cutting edge Most computer architecture books are just too technical and complex Focusing on specific technology they often bypass the basics and are outdated as quickly as technology advances Now Irv Englander's gentle but thorough introduction to computer architecture and systems software provides just the right amount of technical detail you'll need to make successful decisions in your future career The text covers all the basics in an accessible easy to understand way Organized in a form that parallels an actual computer system entire sections are devoted to principles of data hardware and software with computer interconnection clustering and networking integrated into the material to emphasize the importance of computer and system structure Assuming only basic knowledge these sections build up to an in depth understanding of each topic and how they interrelate to make up a computer system With this Third Edition's outstanding features you'll be able to build a solid foundation for success on the job All chapters have been thoroughly updated to reflect current technology Revised with even clearer discussions of virtual storage the operation of memory and modern CPU architectures Programming examples are written in a C Java like pseudocode Emphasizes the computer aspects of clustering and networking rather than the data communication aspects Provide an understanding of underlying non changing basics of computers so that you can make knowledgeable decisions about systems Introduce new technological concepts without overwhelming you with too much detail Examples cover a broad spectrum of hardware and software systems from personal computers to mainframes Integrates discussions of hardware and software throughout and explores the symbiosis between them

International Books in Print, 1997

Proceedings Peter Kornerup, David W. Matula, 1991

Proceedings Computer Arithmetic Algebra OOP

Computer Hardware Description Languages and Their Applications Dominique Borrione, Ron Waxman, 1991 The topic areas presented within this volume focus on design environments and the applications of hardware description and modelling including simulation verification by correctness proofs synthesis and test The strong relationship between the topics of CHDL 91 and the work around the use and re standardization of the VHDL language is also explored The quality of this proceedings and its significance to the academic and professional worlds is assured by the excellent technical programme here compiled

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