

General Purpose Transistors

NPN Silicon

FEATURES

- High current capacity in compact package
- 50, 100, 200
- Epitaxial power type
- JEDEC compliance: TO18
- Pin-Trim Package is available
- ICL: Suitable for Automotive and Other Applications Requiring Integer Size and Control Change Requirements, AEC-Q101 Qualified and PPAP Capable

DEVICE MARKING AND ORDERING INFORMATION

Device	Marking	Shipping
L8050H01TIG	S-L8050H01TIG	5000Tape and Reel
L8050H02TIG	S-L8050H02TIG	10000Tape and Reel
L8050H03TIG	S-L8050H03TIG	5000Tape and Reel
L8050H04TIG	S-L8050H04TIG	10000Tape and Reel
L8050H05TIG	S-L8050H05TIG	5000Tape and Reel
L8050H06TIG	S-L8050H06TIG	10000Tape and Reel
L8050H07TIG	S-L8050H07TIG	5000Tape and Reel
L8050H08TIG	S-L8050H08TIG	10000Tape and Reel

L8050H01TIG Series
S-L8050H01TIG Series



8050-01



MAXIMUM RATINGS

Rating	Symbol	Min	Max
Collector-Emitter Voltage	V_{CE}	25	V
Collector-Base Voltage	V_{CB}	40	V
Collector-Base Voltage	V_{EB}	5	V
Collector Current	I_C	1000	mA (DC)

TERMINAL CHARACTERISTICS

Characteristics	Symbol	Min	Max
Total Device Dissipation (TA = 25°C) (1)	P_D	200	mW
TA = 25°C Duty Cycle above 25°C		0.5	100%
Thermal Resistance, Junction to Ambient	θ_{JA}	50	°C/W
Total Device Dissipation (Ambient Temperature = 25°C) (2)	P_D	500	mW
TA = 25°C Duty Cycle above 25°C		0.5	100%
Thermal Resistance, Junction to Ambient Ambient and Device Temperature	θ_{JA}	41.1	°C/W
	T_c, T_d, T_j	-55 to +150	°C

1. P_D is limited by SOA and SOG curves.

2. Ambient = 25°C and SOG = 20000 to 100000 cycles.

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J Rink



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