

## 8085 MICROPROCESSOR

	B	D	H	SP
LXI	01	11	21	31
INX	03	13	23	33
DCX	0B	1B	2B	3B
DAD	09	19	29	39
LDAX	0A	1A		
STAX	02	12		PSW
PUSH	C5	D5	E5	F5
POP	C1	D1	E1	F1

	Z	NZ	C	NC	PE	PO	M	P	
JMP	C3	C4	C2	D4	D2	E4	E2	FA	F2
CALL	C0	C4	C4	D4	D4	E4	E4	FC	F4
RET	C9	C8	C0	D8	D0	E8	E0	F8	F0

NOP	00	RIM	20	EI	FB
HLT	76	SIM	30	DI	F3

RDKBD	MODAD	MODIDT
02E7	0362	036E

LHLD	2A
SHLD	22
LDA	3A
STA	32
XCHG	EB
XTHL	E3
PCHL	E9
SPHL	F9

WITH ACCUMULATOR							
ADI	ACI	SUI	SBI	ANI	XRI	ORI	CPI
C6	CE	D6	DE	E6	EE	F6	FE

RST							
0	1	2	3	4	5	6	7
C7	CF	D7	DF	E7	EF	F7	FF

IN	DB
OUT	D3

RLC	RRC	RAL	RAR	DAA	CMA	STC	CMC
07	0F	17	1F	27	2F	37	3F

		SOURCE							
DESTINATION	MOV	B	C	D	E	H	L	M	A
	B	40	41	42	43	44	45	46	47
	C	48	49	4A	4B	4C	4D	4E	4F
	D	50	51	52	53	54	55	56	57
	E	58	59	5A	5B	5C	5D	5E	5F
	H	60	61	62	63	64	65	66	67
	L	68	69	6A	6B	6C	6D	6E	6F
	M	70	71	72	73	74	75		77
	A	78	79	7A	7B	7C	7D	7E	7F
	ADD	80	81	82	83	84	85	86	87
ADC	88	89	8A	8B	8C	8D	8E	8F	
SUB	90	91	92	93	94	95	96	97	
SBB	98	99	9A	9B	9C	9D	9E	9F	
ANA	A0	A1	A2	A3	A4	A5	A6	A7	
XRA	A8	A9	AA	AB	AC	AD	AE	AF	
ORA	B0	B1	B2	B3	B4	B5	B6	B7	
CMP	B8	B9	BA	BB	BC	BD	BE	BF	
INR	04	0C	14	1C	24	2C	34	3C	
DCR	05	0D	15	1D	25	2D	35	3D	
MVI	06	0E	16	1E	26	2E	36	3E	

# Sheet Microprocessor 8086 Opcode Sheet

**Chao Zhang**



## Sheet Microprocessor 8086 Opcode Sheet :

**Microprocessor 8086 : Architecture, Programming and Interfacing** Mathur Sunil,2010-12 Primarily intended for the undergraduate students of electronics and communication engineering computer science and engineering and information technology this book skilfully integrates both the hardware and software aspects of the 8086 microprocessor It offers the students an up to date account of the state of the art microprocessors and therefore can be regarded as an incomparable source of information on recently developed microprocessor chips The book covers the advanced microprocessor architecture of the Intel microprocessor family from 8086 to Pentium 4 The text is organized in four parts Part I Chapters 1 7 includes a detailed description of the architecture organization instruction set and assembler directives of microprocessor 8086 Part II Chapters 8 11 discusses the math coprocessor multiprocessing and multiprogramming the different types of data transfer schemes and memory concepts Part III Chapters 12 15 covers programmable interfacing chips with the help of extensive interfacing examples Part IV Chapters 16 18 deals with advanced processors from 80186 to Pentium 4 This well organized and student friendly text should prove to be an invaluable asset to the students as well as the practising engineers **KEY FEATURES** Gives elaborate programming examples to develop the analytical ability of students Provides solved examples covering different types of typical interfacing problems to develop the practical skills of students Furnishes chapter end exercises to reinforce the understanding of the subject **Microprocessor 8085, 8086** Abhishek Yadav,2008 *Microprocessors and Multicore Systems* Atul P. Godse,Dr. Deepali A. Godse,2020-12-01 The book is written for an undergraduate course on the 16 bit 32 bit and 64 bit Intel Processors It provides comprehensive coverage of the hardware and software aspects of 8086 80286 80386 80486 and Pentium Processors The book uses plain and lucid language to explain each topic The book provides the logical method of describing the various complicated concepts and stepwise techniques for easy understanding making the subject more interesting The book begins with an overview of microcomputer structure and operation microprocessor evolution and types and the 8086 microprocessor family It explains the 8086 architecture instruction set instruction timings addressing modes Assembly Language Programming ALP assembler directives standard program structures in 8086 assembly language machine coding for 8086 instructions ALP program development tools 8086 interrupts PIC 8259 and interrupt applications It focuses on features architecture pin description data types addressing modes and newly supported instructions of 80286 and 80386 microprocessors It discusses various operating modes supported by 80386 Real Mode Protected Mode and Virtual 8086 Mode Finally the book focuses on multitasking 80486 architecture and Pentium architecture It describes Pentium superscalar architecture pipelining instruction pairing rules instruction and data cache floating point unit and overview of Pentium II Pentium III and Pentium IV processors *Intel486 Microprocessor Family Programmer's Reference Manual* Intel Corporation,1992 An all in one programmer's guide to the personal computer industry's most powerful chip with information on the Intel 486 DX2

microprocessor Also covers the Intel 486 SX microprocessor for affordable and upgradeable entry level system performance This book is organized in five parts including application programming system programming numeric processing compatibility and the instruction set **I486 Processor Programmer's Reference Manual** Intel Corporation,1990 This

book is written for the high level user interested in details of the i486 microprocessor architecture The book is divided into five major sections application programming system programming numeric processing compatibility and instruction set

*386 DX Microprocessor Programmer's Reference Manual* Intel Corporation,1990 Microprocessors and Microcomputer-Based System Design Mohamed Rafiquzzaman,2021-11-01 Microprocessors and Microcomputer Based System Design Second Edition builds on the concepts of the first edition It discusses the basics of microprocessors various 32 bit microprocessors the 8085 microprocessor the fundamentals of peripheral interfacing and Intel and Motorola microprocessors This edition includes new topics such as floating point arithmetic Program Array Logic and flash memories It covers the popular Intel 80486 80960 and Motorola 68040 as well as the Pentium and PowerPC microprocessors The final chapter presents system design concepts applying the design principles covered in previous chapters to sample problems

**Microprocessors and Microcontrollers, 3rd Edition** R.S Kaler,2019-01-05 It is a complete textbook for anyone interested in all aspects of the microprocessors and microcontrollers family This book is based upon Microprocessor 8085 8086 and Microcontroller 8051 All other related microprocessors and microcontrollers like 80186 80286 80386 Pentium 4 ARM and PIC are also discussed All chapters are described with fundamental objectives A review of important terms and concepts is also given at the end of each chapter that reinforces the idea and material presented Each chapter also has questions and problems Broadly the book deals with Evolution of microprocessor digital concepts number systems and their conversion logic gates and combinational logic and circuits complements multiplexers demultiplexers Flip Flops counters registers analog digital conversion counters registers analog digital conversion Microprocessor 8085 and 8086 architecture pin configuration instructions set stack and subroutines addressing modes interrupts machine cycles and bus timings control signals peripheral I O instructions memory segmentation ag register minimum mode 8086 system and timings assembler directives and operators Interfacing devices data transfer schemes interfacing and I O devices programmable peripheral interface PPI programmable keyboard display interface Intel 8279 centronix parallel communication RS 232C UART programmable interval timer 8253 8254 8257 and 8259 Microprocessor applications seven segment LED display microprocessor based traf c control data acquisition system analog to digital A D converter traf c signal controller digital to analog converter Microprocessor 80XXX architecture pin configuration instructions set addressing modes interrupts multitasking and comparison with different microprocessors Microcontroller 8051 MCS 51 family overview architecture basic registers counters and timers timer counter interrupts serial data input output addressing modes push and pop opcodes instructions set arithmetic operations programming and testing the design real time operatingsystems RTOS ARM AVR and

PIC microcontrollers architecture programming model registers and ags exception and interrupt modes instructions set PIC microcontroller family PIC16F84 microcontroller EEPROM data memory PIC16Cxx microcontroller family Embedded systems programming using Keil software instructions set for 8085 8086 and 8051 *The Advanced Intel Microprocessors* Barry B. Brey,1993 Presents programming interfacing and applications for the 80286 80386 and 80486 Intel microprocessors This text is organized into two parts the microprocessor as a programmable device and the microprocessor within its environment

**Microprocessor System** Saifullah Khalid,Dr. Neetu Agrawal,2009-05 [The Intel 32-bit Microprocessors](#) Barry B. Brey,1995 Coverage first concentrates on real mode assembly language programming compatible with all versions of the Intel microprocessor family and compares and contrasts advanced family member with the foundational 8086 8088 This building block presentation is effective because the Intel family units are so similar that learning advanced versions is easy once the basics are understood **80386 Technical Reference** Edmund Strauss,1987 *The 8088 and 8086*

*Microprocessors* Walter A. Triebel,Avtar Singh,2000 For one semester courses in Microprocessors This text provides a systems level understanding of the 80X86 microprocessor and its hardware and software Equal emphasis is given to both assembly language software and microcomputer circuit design **1993 Product Line Handbooks: Microprocessors (2 v.)** Intel Corporation,1993 [The Intel Microprocessors](#) Barry B. Brey,1994 *Microprocessors* Intel Corporation,1992

**386 SX Microprocessor** Intel Corporation,1989 **8086/8088, 80286, 80386, and 80486 Assembly Language Programming** Barry B. Brey,1994 *IAPX 86, 88 User's Manual* Intel Corporation,1981 *Microcomputer Experimentation with the Intel SDK-86* Lance A. Leventhal,1987 This introductory lab text in microprocessors for engineering science and computer science students shows how to use the Intel R SDK 86 design kit for scientific and engineering applications such as communication instrumentation and process control Leventhal s straightforward approach makes it easy to understand tough concepts for example he provides an introduction to the uses of interfacing chips 8255 and 8251 Each lab is self contained with clear detailed objectives required equipment and key terms Over 70 fully tested examples and over 200 related problems are provided

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