

Semiconductor Devices

Short Notes

PN Junction

P-type: When trivalent impurities are added or doped to pure (intrinsic) semiconductor, p-type semiconductor is formed.

Trivalent Impurities → Gallium, Indium, etc.

N-type: When pentavalent impurities are added or doped to pure semiconductor, n-type semiconductor is formed.

Pentavalent Impurities → Antimony, Arsenic

P-type
Holes (+ve charge)
Majority charge carriers
Electrons (-ve charge)
Minority charge carriers

N-type
Electrons
Majority charge carriers
Holes
Minority charge carriers

Do majority and minority charge carriers vary? → 100% P-type or 100% N-type semi-conductor contains some amount of electron in holes and N-type contains some amount of holes in electrons. So, P-type has electron as minority charge carriers and N-type has holes as minority charge carriers.



If P-type & N-type are attached, diffusion phenomena occurs. Charges get diffused or moved to another end, which creates a potential barrier or depletion layer.



PN Diode (Diode - Device)

Diode is a device made by use of semi-conductors.



Fig: Diode



Fig: Symbolical representation of PN Junction diode

Forward Biasing of Diode



Fig: Forward Biasing. Positive charges have to get holes, and electrons have to get electrons. In the depletion layer, there is a flow of charge, electricity through the diode.



Fig: Reverse Biasing. No flow of electricity. Yes, there is a -ve charge attraction, but the side has accumulated positive charges, so electricity does not flow.

Forward Biasing Characteristics



Fig: Circuit diagram of Forward Biasing



Fig: Forward Characteristics

Series rheostat to help in voltage reduction, current control, yes, meaning potential barrier. Potential barrier voltage supply, hinders. After voltage barrier, potential barrier is overcome.

potential barrier, built-in voltage. As a gradual increase of current flows, at some point the potential barrier sharply increases, and the depletion layer gets smoothly from the junction.

Reverse Biasing Characteristics

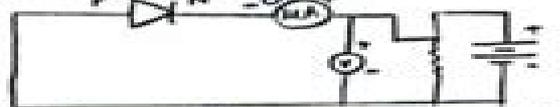


Fig: Circuit diagram of reverse biasing. Reverse voltage.



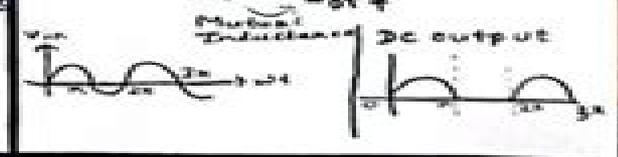
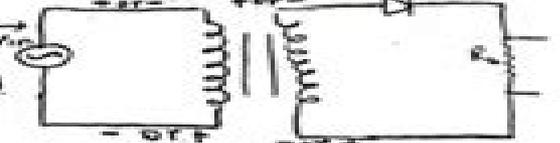
Fig: Reverse Characteristics

Semiconductor is a pure state, but contains both P-type and N-type. P-type has holes as majority charge carriers and N-type has electrons as majority charge carriers. Minority charge carriers (holes in N-type and electrons in P-type) move towards the junction. At some concept, a high voltage is applied, and the breakdown voltage is reached, where the current sharply increases.

Yes, the breakdown voltage is reached, where the current sharply increases.

Rectification: Process of converting AC to DC

1. Half-wave Rectification



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