



ANALOG Circuit Design

Volume 3

Design Note Collection

Edited by Bob Dobkin and John Hamburger



Analog Circuit Design Volume 3

Annelies Wilder-Smith



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converters systems on a chip and RF circuitry are described Part III PLLs and Synthesizers illustrates up to date techniques for combination of inductors on a CMOS chip together with PLL techniques to obtain low noise frequency synthesizers for telecom applications Special attention is paid to fractional N synthesizers using sigma delta algorithms Analog Circuit Design is an essential reference source for analog design engineers and researchers wishing to keep abreast with the latest developments in the field The tutorial nature of the contributions also makes it suitable for use in an advanced design course

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