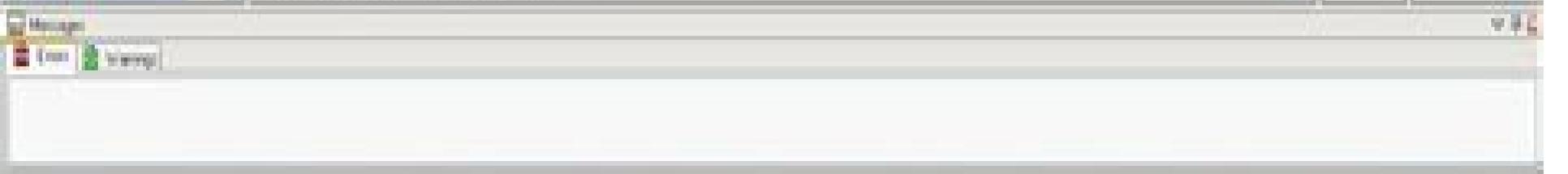




```

37
38
39
40
41
42
43
44
45
46
47
48
49 // External Interrupt(s) initialization
50 // INT0: off
51 // INT1: off
52 // INT2: off
53 // INT3: off
54
55 // Timer/Counter(s) Interrupt(s) initialization
56
57
58 // UART initialization
59 // UART disabled
60
61 // Analog Comparator initialization
62 // Analog Comparator: off

```



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**Ying-Ying Zheng**



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