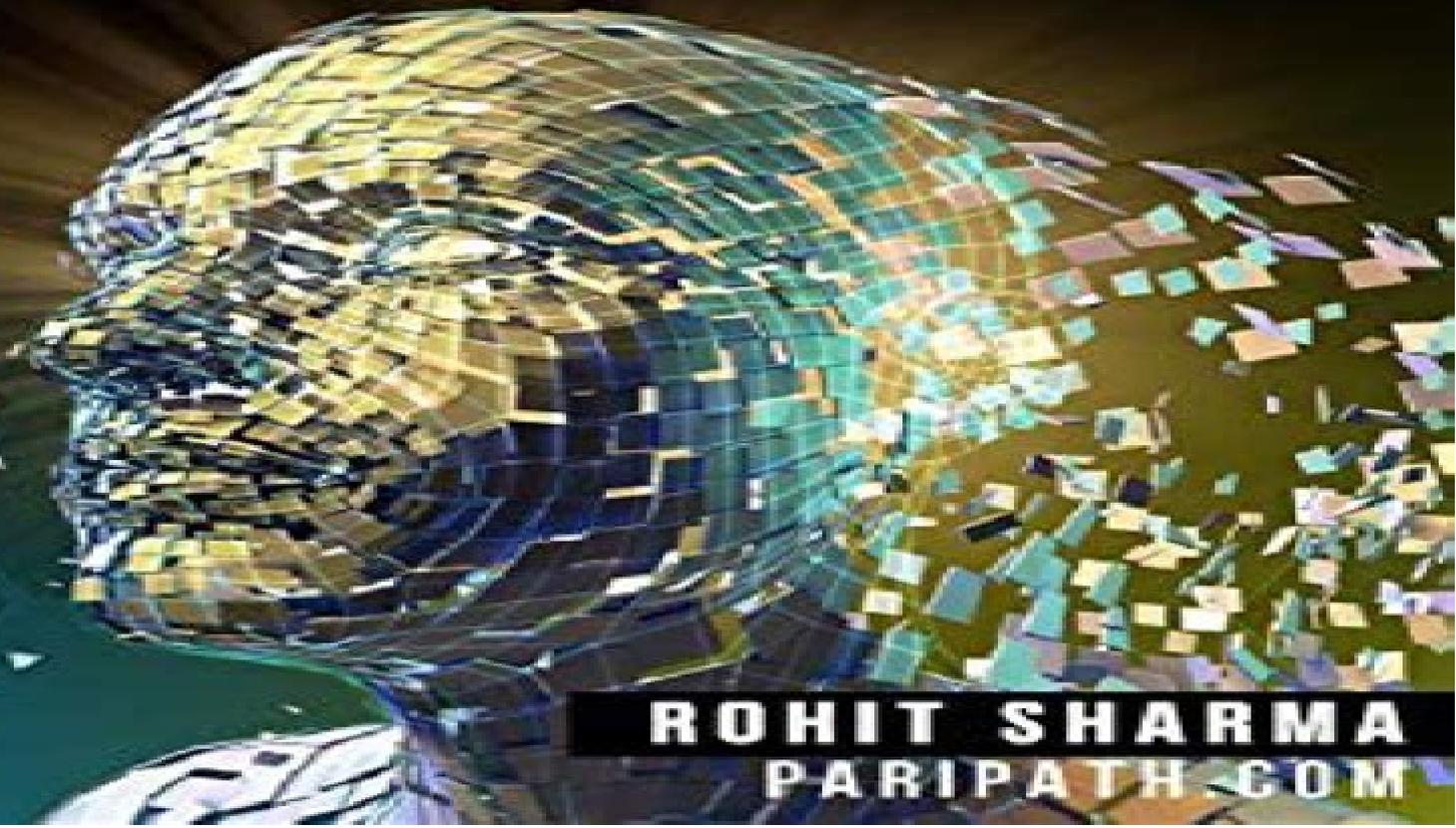


# CHARACTERIZATION AND MODELING OF DIGITAL CIRCUITS



**ROHIT SHARMA**  
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# Characterization And Modeling Of Digital Circuits

**Michael Cody Mitchell**



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**Characterization and Modeling of Digital Circuits Using the Ferroelectric Transistor** Michael Cody Mitchell,2012 *Multi-Threshold CMOS Digital Circuits* Mohab Anis,Mohamed Elmasry,2012-12-06 Multi Threshold CMOS Digital Circuits Managing Leakage Power discusses the Multi threshold voltage CMOS MTCMOS technology that has emerged as an increasingly popular technique to control the escalating leakage power while maintaining high performance The book addresses the leakage problem in a number of designs for combinational sequential dynamic and current steering logic Moreover computer aided design methodologies for designing low leakage integrated circuits are presented The book give an excellent survey of state of the art techniques presented in the literature as well as proposed designs that minimize leakage power while achieving high performance Multi Threshold CMOS Digital Circuits Managing Leakage Power is written for students of VLSI design as well as practicing circuit designers system designers CAD tool developers and researchers It assumes a basic knowledge of digital circuit design and device operation and covers a broad range of circuit design techniques

**Proceedings of the IEEE 1999 Custom Integrated Circuits Conference** ,1999 *Introduction to Microwave Circuits* Robert J. Weber,2001-01-23 Do you want to design a wireless transmitter or receiver for hand held telephones Have you wondered why the printed circuit wires on high frequency circuits don t always run in a straight line This valuable text will answer all of your questions regarding component parasitics and circuit characterization for rf microwave amplifier oscillator and filter circuit design and analysis You will understand why capacitors act as inductors and vice versa and why amplifiers work like oscillators while oscillators for local area networks work more like local area heaters Application of the information in *Introduction to Microwave Circuits* will reduce design cycle time and costs markedly increasing the probability of first time success in printed circuit or monolithic microwave integrated circuit MMIC design Several approaches are taken into consideration such as the effects of currents on the ground plane bypass and coupling capacitors and nonlinear effects in linear circuits Featured topics include

Incorporation of component parasitics in the design cycle Closed form solution to oscillator design Odd mode stability analysis PIN diode analysis for high power switching applications An integrated design example of a 1.25 GHz amplifier oscillator and filter printed circuit is also included which could be useful in printed circuit board designs from tens of megahertz to tens of gigahertz Introduction to Microwave Circuits provides the tools necessary to analyze or synthesize microwave circuits This text is an essential reference for undergraduate students microwave engineers and administrators Also it will assist experienced designers in other fields to meet the current rapid expansion of communication system applications and work effectively in microwave circuit design About the Author Robert J Weber began his prolific career in the Solid State Research Laboratory at the Collins Radio Company later a part of Rockwell International For 25 years he worked on advanced development and applied research in the one to ten gigahertz frequency range and received several distinguished awards for his valuable contributions to the field Dr Weber is involved in ongoing experimental research in integrating microwave circuits with other devices such as MEMS chemical sensors and electro optics Also he teaches microwave circuit design and fiber optics communications at the Department of Electrical and Computer Engineering Iowa State University Dr Weber is an IEEE Fellow Sponsored by IEEE Microwave Theory and Techniques Society *Proceedings of the Technical Conference*, 1992 **VLSI Design Techniques for Analog and Digital Circuits** Randall L. Geiger, Phillip E. Allen, Noel R. Strader, 1990 *Proceedings*, 1990 **Analog MOS Integrated Circuits, II** Paul R. Gray, Bruce A. Wooley, Robert W. Brodersen, 1989-01-15 Read the entire Bible in a year wit Max Lucado You have not been spattered with grace You have not been sprinkled with forgiveness You have not been dusted with kindness You have been immersed in it As a child of God you are submerged in His mercy Welcome God's pure gift Drink deeply from God's endless aquifer of grace Bestselling author and pastor Max Lucado invites you to drench yourself in grace as you spend a few moments each day in God's Word Excerpts from Max's works on topics that are relevant to your life help you connect daily with the Savior and experience the fullness of His grace Each of the 365 readings featured a selection from Grace for the Moment and readings from the Old and New Testaments Psalms and Proverbs Other Great Features Include Each day includes portions of the OT NT Psalms and Proverbs plus a devotional from Max Includes 365 daily readings from the first and second editions of Grace for the Moment Text size point 8 *Analysis and Design of Energy-efficient Digital Circuits* Bart Richard Zeydel, 2006 *GaAs Devices and Circuits* Michael S. Shur, 2013-11-21 GaAs devices and integrated circuits have emerged as leading contenders for ultra high speed applications This book is intended to be a reference for a rapidly growing GaAs community of researchers and graduate students It was written over several years and parts of it were used for courses on GaAs devices and integrated circuits and on heterojunction GaAs devices developed and taught at the University of Minnesota Many people helped me in writing this book I would like to express my deep gratitude to Professor Lester Eastman of Cornell University whose ideas and thoughts inspired me and helped to determine the direction of my research work for many years I also

benefited from numerous discussions with his students and associates and from the very atmosphere of the pursuit of excellence which exists in his group I would like to thank my former and present co workers and colleagues Drs Levinstein and Gelmont of the A F Ioffe Institute of Physics and Technology Professor Melvin Shaw of Wayne State University Dr Kastalsky of Bell Communications Professor Gary Robinson of Colorado State University Professor Tony Valois and Dr Tim Drummond of Sandia Labs for their contributions to our joint research and for valuable discussions My special thanks to Professor Morko for his help his ideas and the example set by his pioneering work Since 1978 I have been working with engineers from Honeywell Inc Drs Comprehensive Dissertation Index ,1989 **Is There an Electrical Engineer**

**Inside You?** Celeste Baine,2004 Specific advice for those considering a career in electrical engineering IEEE Transactions on Circuits and Systems ,2006 40th Electronic Components & Technology Conference ,1990 **Circuit and Layout Techniques for Soft-error-resilient Digital CMOS Circuits** Hsiao-Heng Kelin Lee,2011 Radiation induced soft errors are a major concern for modern digital circuits especially memory elements Unlike large Random Access Memories that can be protected using error correcting codes and bit interleaving soft error protection of sequential elements i e latches and flip flops is challenging Traditional techniques for designing soft error resilient sequential elements generally address single node errors or Single Event Upsets SEUs However with technology scaling the charge deposited by a single particle strike can be simultaneously collected and shared by multiple circuit nodes resulting in Single Event Multiple Upsets SEMUs In this work we target SEMUs by presenting a design framework for soft error resilient sequential cell design with an overview of existing circuit and layout techniques for soft error mitigation and introducing a new soft error resilience layout design principle called LEAP or Layout Design through Error Aware Transistor Positioning We then discuss our application of LEAP to the SEU immune Dual Interlocked Storage Cell DICE by implementing a new sequential element layout called LEAP DICE retaining the original DICE circuit topology We compare the soft error performance of SEU immune flip flops with the LEAP DICE flip flop using a test chip in 180nm CMOS under 200 MeV proton radiation and conclude that 1 our LEAP DICE flip flop encounters on average 2 000X and 5X fewer errors compared to a conventional D flip flop and our reference DICE flip flop respectively 2 our LEAP DICE flip flop has the best soft error performance among all existing SEU immune flip flops 3 In the evaluation of our design framework we also discovered new soft error effects related to operating conditions such as voltage scaling clock frequency setting and radiation dose **IBM Journal of Research and Development** ,2003

**BiCMOS Integrated Circuit Design** IEEE Solid-State Circuits Council,1994 This up to date book with its introductory tutorial provides extensive coverage on BiCMOS potentially the most important silicon technology of the 1990 s Many commercial products are currently utilizing BiCMOS ICs and an exponential growth is expected Elmasry provides the latest information on processing technologies circuit analysis and techniques and applications in the areas of analog digital and smart power BiCMOS Integrated Circuit Design is an important guide for engineers working in BiCMOS processing modeling

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