PMBus Connector

XADC Header

User SMA Clock

User SMA

User LEDs

SFP/SFP+ Connector

FMC (HPC, 4x GTX)

FMC (LPC, 1X GTX)

SD Card Interface

Linear BPI Flash Memory (128MB)

Power Switch

Config Mode/Upper Linear Flash Address Switch

12V Power

USB JTAG Interface

User DIP Switch

Status LEDs

FPGA Prog Push Button

10/100/1000 MHz Ethernet PHY

HDMI Output

User Push Buttons

GTX SMA Transceiver and Clock

PCI Express Connector (8-lane)

Kintex-7 XC7K325T-2FFG900C FPGA

User Rotary Switch (located under LCD)

DDR3 SODIMM Memory (1GB)

LCD Character Display

Kintex 7 Fpga Embedded Targeted Reference Design

S Ashworth

Kintex 7 Fpga Embedded Targeted Reference Design:

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Table of Contents Kintex 7 Fpga Embedded Targeted Reference Design

Kintex 7 Fpga Embedded Targeted Reference Design Introduction

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